

Form PTO-1449 (modified)

Atty. Docket No.

MCRO:264/KRE

Serial No.

Unassigned

List of Patents and Publications for Applicant's

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

Applicant

TIANHONG ZHANG and ZHONGYI XIA

Filing Date:

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Group:

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U.S. Patent Documents

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Foreign Patent Documents

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U.S. Patent Documents

| Exam. Init. | Ref. Des. | Document Number | Date | Name | Class | Sub Class | Filing Date of App. |
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| DD | A1 | 5,372,973 | 12/13/94 | Doan et al. | 437 | 228 | 04/27/93 |
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EXAMINER:

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| U.S. Patent Documents See Page 1 | Foreign Patent Documents See Page 2 | Other Art See Page 2 | |

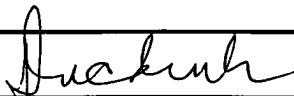
Foreign Patent Documents

| Exam. Init. | Ref. Des. | Document Number | Date | Country | Class | Sub Class | Translation Yes/No |
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| DD | C1 | Institute of Electrical And Electronic Engineers, Inc.; Hot Carrier Self Convergent Programming Method for Multi-Level Flash Cell Memory; 1997; pgs. 104-109; 35 th Annual IEEE (1997) International Reliability Physics Symposium Proceedings. |
| DD | C2 | H.S. Kim, J.D. Choi, J. Kim, W.C. Shin, D.J. Kim, K.M. Mang, and S.T. Ahn; Fast Parallel Programming of Multi-Level NAND Flash Memory Cells Using the Booster-Line Technology; 1997; pgs. 65-66; (1997) Symposium on VLSI Technology Digest of Technical Papers. |
| DD | C3 | H. Gotou; An Experimental Confirmation of Automatic Threshold Voltage Convergence in a Flash Memory Using Alternating Word-Line Voltage Pulses; October, 1997; pgs. 503-505; IEEE Electron Device Letters, Vol. 18, No. 10. |
| DD | C4 | DialogTech; DialogTech Titles List in Engineering & Electronics Research; 1997; 3 pages. |
| DD | C5 | Min-hwa Chi and Albert Bergemont; Multi-level Flash/EPROM Memories: New Self-convergent Programming Method for Low-voltage Applications; 1995; pgs. 271-274. |
| DD | C6 | Walter F. Kosonocky; Review of Infrared Image Sensors with Schottky-Barrier Detectors; October 22, 1991; pgs. 173-203; Optoelectronics – Devices and Technologies, Vol. 6, No. 22. |
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